

5.0 SATA Interface

These drives use the industry-standard Serial ATA (SATA) interface that supports FIS data transfers. It supports ATA programmed input/output (PIO) modes 0 to 4; multiword DMA modes 0 to 2, and Ultra DMA modes 0 to 6.

For detailed information about the SATA interface, refer to the “Serial ATA: High Speed Serialized AT Attachment” specification.

5.1 Hot-Plug compatibility

BarraCuda drives incorporate connectors which enable users to hot plug these drives in accordance with the SATA Revision 3.2 specification. This specification can be downloaded from www.serialata.org.

5.2 SATA device plug connector pin definitions

Table 8 summarizes the signals on the SATA interface and power connectors.

Table 8 SATA connector pin definitions

Segment	Pin	Function	Definition
Signal	S1	Ground	2nd mate
	S2	A+	Differential signal pair A from Phy
	S3	A-	
	S4	Ground	2nd mate
	S5	B-	Differential signal pair B from Phy
	S6	B+	
	S7	Ground	2nd mate
Key and spacing separate signal and power segments			
Power	P1	V ₃₃	3.3V power
	P2	V ₃₃	3.3V power
	P3	V ₃₃	3.3V power, pre-charge, 2nd mate
	P4	Ground	1st mate
	P5	Ground	2nd mate
	P6	Ground	2nd mate
	P7	V ₅	5V power, pre-charge, 2nd mate
	P8	V ₅	5V power
	P9	V ₅	5V power
	P10	Ground	2nd mate
	P11	Ground or LED signal	If grounded, drive does not use deferred spin
	P12	Ground	1st mate.
	P13	V ₁₂	12V power, pre-charge, 2nd mate
	P14	V ₁₂	12V power
	P15	V ₁₂	12V power

Notes

- All pins are in a single row, with a 1.27 mm (0.050 in) pitch.
- The comments on the mating sequence apply to the case of backplane blindmate connector only. In this case, the mating sequences are:
 - the ground pins P4 and P12.
 - the pre-charge power pins and the other ground pins.
 - the signal pins and the rest of the power pins.
- There are three power pins for each voltage. One pin from each voltage is used for pre-charge when installed in a blind-mate backplane configuration.
 - All used voltage pins (V_x) must be terminated.